

Cookson Materials Testing
 StencilQuick™ SIR Testing per JSTD-004
 NO PASTE Test Series #1

Test #:0427-1i Date: 3/8/2005 Temp/Humidity/Bias 85°C/85%RH/-48V
 Tested by: L. Herbert Jr. Pass/Fail limit: 1.0E+08 ohms
 Reported by: K. Tellefsen

MATERIAL	SIR (ohms 1 day) -----	SIR (ohms 4 days) -----	SIR (ohms 7 days) -----	COMMENTS
-Stencilquick	2.8E+10	8.6E+09	1.4E+10	passed electrical and visual requirements
-No Paste	3.7E+10	1.2E+10	2.2E+10	
-Reflowed	2.6E+10	7.8E+09	1.3E+10	
-Uncleaned	3.9E+10	1.1E+10	2.2E+10	
	1.8E+10	6.3E+09	9.4E+09	
	2.7E+10	7.6E+09	1.4E+10	
	8.2E+09	7.0E+09	2.0E+09	
	2.1E+10	7.2E+09	8.1E+09	
	2.3E+10	8.4E+09	1.1E+10	
	4.1E+10	1.5E+10	1.7E+10	
	2.1E+10	8.1E+09	1.1E+10	
	2.4E+10	8.8E+09	1.2E+10	
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Arithmetic mean:	2.6E+10	9.0E+09	1.3E+10	

Stencilquik				
-No paste				
-Reflowed	1.8E+10	7.8E+09	1.1E+10	passed electrical and visual requirements
-Uncleaned	3.3E+10	1.5E+10	1.7E+10	
	1.7E+10	7.2E+09	9.6E+09	
	2.1E+10	8.8E+09	1.3E+10	

Control boards	1.3E+11	4.3E+10	3.2E+10	passed electrical and visual requirements
	1.9E+11	8.6E+10	5.6E+10	
	1.2E+11	3.2E+10	2.9E+10	
	1.8E+11	5.1E+10	3.7E+10	
	3.9E+10	2.4E+10	2.0E+10	
	7.1E+10	2.5E+10	2.6E+10	
	3.2E+10	2.2E+10	1.7E+10	
	4.1E+10	2.5E+10	2.2E+10	
	3.6E+10	2.1E+10	1.8E+10	
	6.3E+10	2.9E+10	2.4E+10	
	1.7E+10	2.0E+10	1.7E+10	
	4.5E+10	2.7E+10	1.5E+10	
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Arithmetic mean:	9.7E+10	3.4E+10	2.6E+10	

Cookson Materials Testing

Stencil Quick™ SIR Testing per JSTD-004

Cookson OM338 Lead-Free\No Clean Solder Paste, ROLO Flux Test Series #2

Test #:0427-2i Date: 3/8/2005

Temp/Humidity/Bias 85°C/85%RH/-48V

Tested by: L. Herbert Jr.

Pass/Fail limit: 1.0E + 08 ohms

Reported by: K. Tellefsen

MATERIAL	SIR (ohms 1 day) -----	SIR (ohms 4 days) -----	SIR (ohms 7 days) -----	COMMENTS
Stencilquik	3.8E+09	3.0E+09	5.4E+09	passed and visual requirements
-Reflowed	7.4E+09	5.4E+09	8.2E+09	
-Uncleaned	3.5E+09	2.8E+09	4.9E+09	
	3.2E+09	2.6E+09	4.7E+09	
	7.4E+09	3.3E+09	6.6E+09	
	6.8E+09	3.1E+09	6.4E+09	
	2.5E+09	2.0E+09	3.5E+09	
	3.1E+09	2.2E+09	3.9E+09	
	4.0E+09	3.3E+09	5.4E+09	
	7.6E+09	5.2E+09	8.2E+09	
	3.7E+09	2.7E+09	4.8E+09	
	3.1E+09	2.6E+09	4.9E+09	
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Arithmetic mean:	4.7E+09	3.2E+09	6.6E+09	
Control boards	1.3E+11	4.3E+10	3.2E+10	
	3.9E+11	8.6E+10	5.6E+10	
	1.2E+11	3.2E+10	2.9E+10	
	1.8E+11	5.1E+10	3.7E+10	
	3.9E+10	2.4E+10	2.0E+10	
	7.1E+10	2.5E+10	2.6E+10	
	3.2E+10	2.2E+10	1.7E+10	
	4.1E+10	2.5E+10	2.2E+10	
	3.6E+10	2.1E+10	1.8E+10	
	6.3E+10	2.9E+10	2.4E+10	
	1.7E+10	2.0E+10	1.7E+10	
	4.5E+10	2.7E+10	1.5E+10	
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Arithmetic mean:	9.7E+10	3.4E+10	2.6E+10	

Cookson Materials Testing
 Stencil Quick™ SIR Testing per JSTD-004
 Cookson OM5000 Lead-Free Solder Paste Test Series #3

SIR TEST PER J-STD-004

Test #:0427-3i Date: 3/8/2005 Temp/Humidity/Bias 85°C/85%RH/-48V
 Tested by: L. Herbert Jr. Pass/Fail limit: 1.0E +08 ohms
 Reported by: K. Tellefsen

MATERIAL	SIR (ohms 1 day) -----	SIR (ohms 4 days) -----	SIR (ohms 7 days) -----	COMMENTS
Stencilquik	4.4E+09	3.0E+09	5.7E+09	passed and visual requirements
-Reflowed	9.0E+09	5.6E+09	9.0E+09	
-Uncleaned	4.3E+09	3.6E+09	6.5E+09	
	3.8E+09	2.5E+09	4.7E+09	
	4.2E+09	2.5E+09	5.7E+09	
	6.8E+09	3.5E+09	8.1E+09	
	4.1E+09	2.5E+09	4.7E+09	
	3.7E+09	2.3E+09	1.6E+06	
	4.2E+09	2.7E+09	6.7E+09	
	2.0E+10	2.8E+10	1.2E+10	
	1.8E+10	6.2E+09	7.6E+09	
	3.7E+09	2.4E+09	6.2E+09	
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Arithmetic mean:	7.2E+09	5.4E+09	6.4E+09	
Control boards	1.3E+11	4.3E+10	3.2E+10	
	3.9E+11	8.6E+10	5.6E+10	
	1.2E+11	3.2E+10	2.9E+10	
	1.8E+11	5.1E+10	3.7E+10	
	3.9E+10	2.4E+10	2.0E+10	
	7.1E+10	2.5E+10	2.6E+10	
	3.2E+10	2.2E+10	1.7E+10	
	4.1E+10	2.5E+10	2.2E+10	
	3.6E+10	2.1E+10	1.8E+10	
	6.3E+10	2.9E+10	2.4E+10	
	1.7E+10	2.0E+10	1.7E+10	
	4.5E+10	2.7E+10	1.5E+10	
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Arithmetic mean:	9.7E+10	3.4E+10	2.6E+10	